About Arm Academic Access

Arm Academic Access simplifies free licensing to universities with an institutional agreement. IP is provided on a needs basis and can be extended to different research groups and/or in scope simply and easily.

For no fee, you can accelerate your research using Arm's extensive portfolio of IP and join one of the world's largest and most innovative research ecosystems.
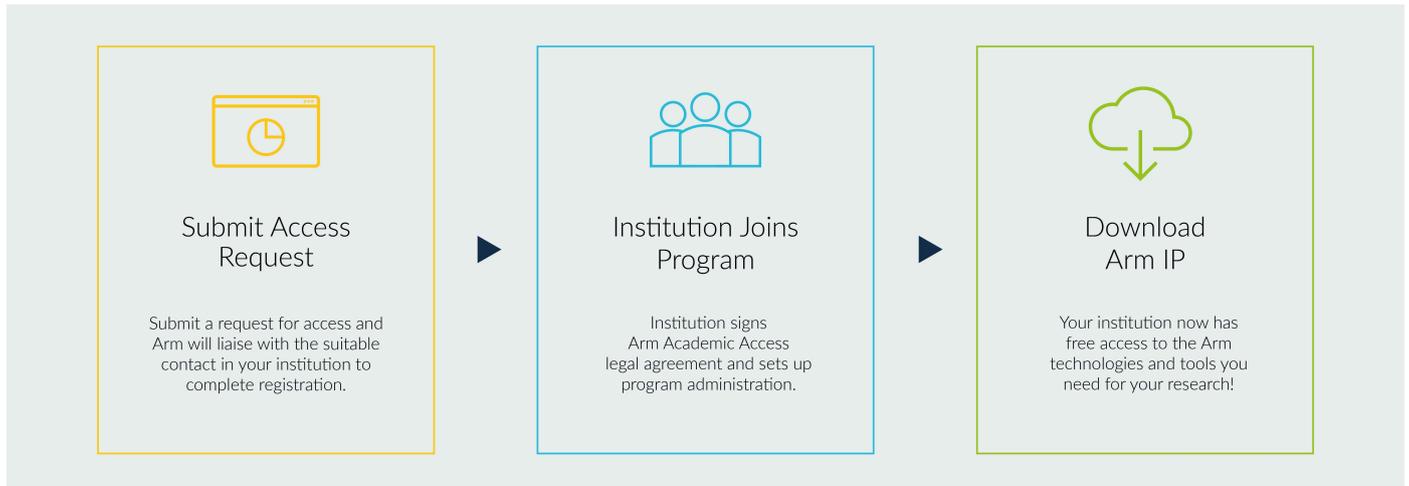
For more information, contact the Arm Research Enablement team today.

What can be included in Arm Academic Access?

In addition to the most comprehensive Artisan physical IP library for those who need it, Arm Academic Access can include the following products. Our team will discuss your needs and identify the most suitable place to start.

Access to entire mainstream product package, including CPUs, GPUs, Corstone foundation IP and system IP	Y
Number of online training seats for introductory training on a selection of mainstream IP products	5
Seats of Arm Development Studio (Arm DS Gold)	10
Seats of Arm configuration tool - Socrates	10
Seats of Arm virtual system models (fixed and cycle-accurate)	1
Arm Service Tokens (can be used for training, design reviews and onsite support)	**Available separately on a case-by-case basis**

How does Arm Academic Access work?

Submit Access Request	Institution Joins Program	Download Arm IP
Submit a request for access and Arm will liaise with the suitable contact in your institution to complete registration.	Institution signs Arm Academic Access legal agreement and sets up program administration.	Your institution now has free access to the Arm technologies and tools you need for your research!

Product	Description	Learn more about this product
CPU Processing		
Cortex-A53 Processor	Low-power processor with 64-bit capabilities, applicable in a range of devices requiring high performance in power-constrained environments.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a53
Cortex-A35 Processor	Ultra-high efficiency smart home processor, the smallest and most power-efficient 32-bit and 64-bit Arm application processor.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a35
Cortex-A34 Processor	Low-power 64-bit only processor with ultra-high efficiency.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a34
Cortex-A32 Processor	Low-power 32-bit only processor with ultra-high efficiency, suitable for diverse embedded and IoT markets.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a32
Cortex-A7 Processor	Power-efficient processor, designed for a wide range of devices with differing requirements demanding balance between power and performance.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a7
Cortex-A5 Multi-Processor	Smallest Cortex-A processor designed for applications that require virtual memory management for high-level operating systems within a low-power, low-area profile.	https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a5
Cortex-A5 Uni-Processor		https://www.arm.com/products/silicon-ip-cpu/cortex-a/cortex-a5
Cortex-R52 Processor	Designed for advanced silicon processes requiring high-performance and cost-effective processing. It delivers real-time performance for functional safety.	https://www.arm.com/products/silicon-ip-cpu/cortex-r/cortex-r52
Cortex-R8 Processor	Designed for products where performance requirements and timing deadlines must always be met, or where functional safety is critical.	https://www.arm.com/products/silicon-ip-cpu/cortex-r/cortex-r8
Cortex-R5 Processor	Offers high-performance computing solutions for embedded systems needing reliability, high availability, fault tolerance, and real-time responses.	https://www.arm.com/products/silicon-ip-cpu/cortex-r/cortex-r5

Cortex-M33 Processor	Optimized for cost and power-sensitive microcontroller and mixed-signal applications. Designed to address embedded and IoT markets, especially those requiring efficient security or digital signal control.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m33
Cortex-M23 Processor	Smallest and lowest-power microcontroller with Arm TrustZone security, ideal for applications requiring software isolation and software security.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m23
Cortex-M7 Processor	The highest performance member of the energy-efficient Cortex-M processor family.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m7
Cortex-M4 Processor	Designed to address digital signal control markets that demand an efficient, easy-to-use blend of control and signal processing capabilities.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m4
Cortex-M3 Processor	Designed for cost-sensitive and power-constrained solutions in a broad range of devices. Balanced between area, performance, and power.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m3
Cortex-M0+ Processor	The smallest footprint and lowest power requirements of all the Cortex-M processors, suitable for a wide variety of applications, including sensors and wearables.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m0-plus
Cortex-M0 Processor	Designed for smart and connected embedded applications, the smallest Arm processor available, ideal for simple, cost-sensitive devices.	https://www.arm.com/products/silicon-ip-cpu/cortex-m/cortex-m0

GPU Processing

Mali-G52 Graphics Processor	Designed to bring premium visual experiences to mainstream markets, Mali-G52 provides heightened machine learning capabilities.	https://www.arm.com/products/silicon-ip-multimedia/gpu/mali-g52
Mali-G31 Graphics Processor	The Arm Mali-G31 GPU is the first ultra-efficient GPU based on the Bifrost architecture targeting smaller applications such as IoT.	https://www.arm.com/products/silicon-ip-multimedia/gpu/mali-g51

CoreLink Interconnect

CoreLink NIC-450 Network Interconnect	Offers highly configurable topology with network-on-chip properties for building high-performance, optimized, AMBA-compliant SoC connectivity, including QoS and Thin links.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/nic
CoreLink NIC-400 Network Interconnect	Offers highly configurable topology with network-on-chip properties for building high-performance, optimized, AMBA-compliant SoC connectivity	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/nic
CoreLink CCI-400 Cache Coherent Interconnect	Provides full cache coherency between two clusters of multi-core CPUs. It enables big.LITTLE processing and I/O coherency for devices.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/cci-400
CoreLink CCI-500 Cache Coherent Interconnect	Full coherency with up to four clusters including big.LITTLE and coherent accelerators. Higher performance and power efficiency with integrated snoop filter.	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/cci-500

CoreLink CCI-550 Cache Coherent Interconnect	Full coherency with up to six clusters including big.LITTLE and coherent accelerators. Higher performance and power efficiency with integrated snoop filter	https://www.arm.com/products/silicon-ip-system/corelink-interconnect/cci-550
CoreLink ADB-400 AMBA Domain Bridge	An asynchronous bridge between two components or systems that can be in a different power, clock, or voltage domains.	https://developer.arm.com/ip-products/system-ip/corelink-interconnect/corelink-network-interconnect-family
CoreLink XHB-400 AXI4- AHB Bridge	Converts AXI4 protocol to AHB-Lite protocol and has an AXI4 slave interface and an AHB-Lite master interface.	https://developer.arm.com/ip-products/system-ip/corelink-interconnect/corelink-network-interconnect-family

System Controllers

CoreLink DMA-330 AXI DMA Controller	A high-performance DMA controller that can boost the performance and reduce the power consumption in AXI-based systems.	https://www.arm.com/products/silicon-ipsystem/embedded-system-design/dma-330
CoreLink DMA-230 AHB Micro DMA Controller	Low gate count (3-10k gates) micro-DMA engine targeting AHB-based Cortex-M systems.	https://www.arm.com/products/silicon-ipsystem/embedded-system-design/dma-230
CoreLink GIC-500 Generic Interrupt Controller	Detects, manages, virtualizes, and distributes interrupts for Armv8.0-A processors. Configurable up to 128 single-threaded cores and 960 shared interrupts.	https://www.arm.com/products/silicon-ip-system/system-controllers/gic
CoreLink GIC-400 Generic Interrupt Controller	Detects, manages, and virtualizes interrupts for Armv7 processors. Configurable up to 8 cores and 480 shared interrupts.	https://www.arm.com/products/silicon-ip-system/system-controllers/gic
PL192 Vectored Interrupt Controller	An advanced vectored interrupt controller supporting up to 32 vectored interrupts with programmable priority level and masking.	https://developer.arm.com/ip-products/system-ip/system-controllers/peripheral-controllers
CoreLink TZC-400 TrustZone Address Space Controller	Performs security checks on transactions to memory or peripherals, configurable wup to 8 regions.	https://www.arm.com/products/silicon-ip-security/address-space-controllers
CoreLink L2C-310 AXI Level 2 Cache Controller	High-performance, AXI level 2 cache controller designed and optimized to address Arm AXI processors, normally used with Cortex-A5.	https://www.arm.com/products/silicon-ip-system/embedded-system-design/l2c-310
CoreLink MMU-500 System Memory Management Unit	System memory management unit that includes caching and memory virtualization. It enforces memory protection and access, and is designed for use in a virtualized system where multiple guest operating systems are managed by a hypervisor. Supports Armv8-A and Armv7-A.	https://www.arm.com/products/silicon-ip-system/system-controllers/mmu
BP140 AXI Internal Memory Interface	AXI to on-chip SRAM interface.	https://developer.arm.com/docs/dto0009/a
BP141 TrustZone AXI Memory Interface	AXI to on-chip SRAM interface with support for Arm TrustZone protection for secure memory regions.	https://developer.arm.com/products/system-ip/system-controllers/other-system-controllers

Security IP

True Random Number Generator	A mandatory component in any system that generates cryptographic assets.	https://www.arm.com/products/silicon-ip-security/random-number-generator

Peripheral Controllers

PL011 UART Universal Asynchronous Receiver/Transmitter	Peripheral controllers for UART, SPI and real-time clock.	https://developer.arm.com/ip-products/system-ip/system-controllers
PL022 SPI Synchronous Serial Port		https://developer.arm.com/ip-products/system-ip/system-controllers
PL031 RTC Real Time Clock		https://developer.arm.com/ip-products/system-ip/system-controllers

CoreSight Debug & Trace

CoreSight SoC-400 Debug and Trace	Offers configurable components, including debug access, trace generation manipulation and output, cross triggering, and time stamping.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/soc-400
CoreSight SDC-600 Secure Debug Channel	Addresses device security needs by allowing silicon and tool vendors to enforce protection and police debug access, and by working closely with cryptographic elements and debug certificate authentication.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/sdc-600
CoreSight STM-500 System Trace Macrocell	Trace source for real-time software instrumentation with no impact on system behaviour or performance. It extends the low-cost, real-time visibility of software and hardware execution to all software developers. Supports 64-bit memory interfaces.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/coresight-stm-500
CoreSight System Trace Macrocell	System Trace Macrocell supporting 32-bit memory interfaces.	https://developer.arm.com/ip-products/system-ip/coresight-debug-and-trace/coresight-components/system-trace-macrocell
CoreSight Trace Memory Controller	A configurable trace component to terminate trace buses into buffers, FIFOs, or alternatively, to route trace data over AXI to memory or off-chip to interface controllers.	https://www.arm.com/products/silicon-ip-system/coresight-debug-trace/coresight-tmc
Coresight SoC-600 Debug and Trace	For high-bandwidth debug and trace solutions. Includes remote and local debug access, trace routing and termination, crosstriggering and time stamping.	https://www.arm.com/products/silicon-ip-system/ coresight-debug-trace/soc-600

Design Kits

Corstone-101 foundation IP	Contains a pre-integrated subsystem and system IP bringing together all core elements for an SoC. Includes the CoreLink SSE-050 subsystem built around a Cortex-M3 processor. Other elements include CMSDK, AHB Flash Cache, RTC, TRNG and a generic eFlash controller.	https://www.arm.com/products/iot/soc/corstone-101
Corstone-201 foundation IP	Incorporates the Arm SSE-200 subsystem for Cortex-M33 and the SSE-123 example subsystem built around the Cortex-M23. The subsystems provide a solid base for either mainstream or constrained device SoC design, with Arm TrustZone support for deep-rooted security.	https://www.arm.com/products/iot/soc/corstone-201

Corstone-500 foundation IP	Corstone-500 offers a proven and preintegrated reference package along with an extensive set of System IP, for building Linuxcapable, high-performance SoCs, based on Arm Cortex-A5. It includes FPGA image, out-of-box Linux support and simulation models, thus accelerating time to market and reducing development risk.	https://www.arm.com/products/iot/soc/corstone-500
Corstone-102 Foundation IP	Arm Corstone-102 provides a flexible reference package and system IP for small, low cost and energy efficient SoC development. Based on the Arm Cortex-M23 processor, the Corstone-102 is targeted at the constrained market segment for secure IoT applications.	https://www.arm.com/products/iot/soc/corstone-102
Corstone-201 foundation IP Virtual system models		
Virtual system models	Fixed configuration systems for select CPUs within themainstream package for benchmarking, performance analysis, and software development ahead of silicon. Fast and Cycle Model variants for each system. Software packages include prepackaged benchmarks and the ability to add own software.	https://www.arm.com/products/developmenttools/simulation/fixed-virtual-platforms

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