

arm SystemReady Certificate of Compliance

Arm SystemReady certification: SystemReady IR v1.2 (32b level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Company: [STMicroelectronics](#)

System: [STMicroelectronics STM32MP157C-EV1 Evaluation board](#)

SoC family: [STM32MP157C](#)

Errata: [Certification Errata](#)

Firmware version: [ST firmware, version 1.0-15936 \(U-Boot 2022.10\)](#)

ACS version: [ACS-IR 32b v0.7 BETA 0](#)

BSA details: Not applicable

BBR details: EBBR v2.0.1 (BBR v1.0)

Tested operating systems:

- Fedora Minimal 36
- Fedora Server 36

Date issued: 10/21/2022



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Company:	STMicroelectronics
System:	STMicroelectronics STM32MP157C-DK2 Discovery kit
SoC family:	STM32MP157C
Errata:	Certification Errata
Firmware version:	ST firmware, version 1.0-15936 (U-Boot 2022.10)
ACS version:	ACS-IR 32b v0.7 BETA 0
BSA details:	Not applicable
BBR details:	EBBR v2.0.1 (BBR v1.0)
Tested operating systems:	<ul style="list-style-type: none">• Fedora Minimal 36
Date issued:	10/24/2022

