Arm SystemReady certification: SystemReady IR v2.0 (level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Company:	Advantech
System:	TPC-100W (TPC-110WX-N32YA)
SoC family:	NXP i.MX8M Mini
Errata:	<u>Certification errata</u>
Firmware version:	Advantech U-Boot based on U-Boot v2023.04 and TF-A v2.6: U-Boot 2023.04 20230915
ACS version:	SystemReady IR ACS v23.03_2.0.0
SRS details:	SRS v2.1
BSA details:	BSA v1.0c
BBR details:	EBBR v2.1.0 (BBR v2.0)
Tested operating systems:	Operating systems that were tested successfully for SystemReady compliance (as required by Arm SystemReady Requirements Specification) but are not necessarily supported on this system: • Debian 12.2.0 12.2.0 • Ubuntu Live Server image 22.04.5 • openSUSE Leap 15.6





Arm SystemReady certification: SystemReady IR v2.0 (level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Advantech Company: TPC-100W (TPC-107W-N32YB) System: SoC family: NXP i.MX8M Mini Errata: Certification errata Advantech U-Boot based on U-Boot v2023.04 and Firmware version: TF-A v2.6: U-Boot 2023.04 20230915 **ACS** version: SystemReady IR ACS v23.03_2.0.0 SRS v2.1 SRS details: **BSA** details: BSA v1.0c **BBR** details: EBBR v2.1.0 (BBR v2.0) Operating systems that were tested successfully for SystemReady **Tested operating systems:** compliance (as required by Arm SystemReady Requirements Specification) but are not necessarily supported on this system: • Debian 12.2.0 12.2.0 • Ubuntu Live Server image 22.04.5 • openSUSE Leap 15.6





Arm SystemReady certification: SystemReady IR v2.0 (level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Advantech Company: TPC-100W (TPC-107WX-N32YA) System: SoC family: NXP i.MX8M Mini Errata: Certification errata Advantech U-Boot based on U-Boot v2023.04 and Firmware version: TF-A v2.6: U-Boot 2023.04 20230915 **ACS** version: SystemReady IR ACS v23.03_2.0.0 SRS v2.1 SRS details: **BSA** details: BSA v1.0c **BBR** details: EBBR v2.1.0 (BBR v2.0) Operating systems that were tested successfully for SystemReady **Tested operating systems:** compliance (as required by Arm SystemReady Requirements Specification) but are not necessarily supported on this system: • Debian 12.2.0 12.2.0 • Ubuntu Live Server image 22.04.5 • openSUSE Leap 15.6





Arm SystemReady certification: SystemReady IR v2.0 (level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Advantech
TPC-100W (TPC-115W-N32YB)
NXP i.MX8M Mini
<u>Certification errata</u>
Advantech U-Boot based on U-Boot v2023.04 and TF-A v2.6: U-Boot 2023.04 20230915
SystemReady IR ACS v23.03_2.0.0
SRS v2.1
BSA v1.0c
EBBR v2.1.0 (BBR v2.0)
Operating systems that were tested successfully for SystemReady compliance (as required by Arm SystemReady Requirements Specification) but are not necessarily supported on this system: Debian 12.2.0 12.2.0 Ubuntu Live Server image 22.04.5 openSUSE Leap 15.6





Arm SystemReady certification: SystemReady IR v2.0 (level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Company:	Advantech
System:	TPC-100W (TPC-121W-N32YB)
SoC family:	NXP i.MX8M Mini
Errata:	Certification errata
Firmware version:	Advantech U-Boot based on U-Boot v2023.04 and TF-A v2.6: U-Boot 2023.04 20230915
ACS version:	SystemReady IR ACS v23.03_2.0.0
SRS details:	SRS v2.1
BSA details:	BSA v1.0c
BBR details:	EBBR v2.1.0 (BBR v2.0)
Tested operating systems:	Operating systems that were tested successfully for SystemReady compliance (as required by Arm SystemReady Requirements Specification) but are not necessarily supported on this system: • Debian 12.2.0 12.2.0 • Ubuntu Live Server image 22.04.5 • openSUSE Leap 15.6

11/12/2024



Date issued:



Arm SystemReady certification: SystemReady IR v2.0 (level 1)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Advantech Company: TPC-100W (TPC-110W-N32YB) System: SoC family: NXP i.MX8M Mini Errata: Certification errata Advantech U-Boot based on U-Boot v2023.04 and Firmware version: TF-A v2.6: U-Boot 2023.04 20230915 **ACS** version: SystemReady IR ACS v23.03_2.0.0 SRS v2.1 SRS details: **BSA** details: BSA v1.0c **BBR** details: EBBR v2.1.0 (BBR v2.0) Operating systems that were tested successfully for SystemReady **Tested operating systems:** compliance (as required by Arm SystemReady Requirements Specification) but are not necessarily supported on this system: • Debian 12.2.0 12.2.0 • Ubuntu Live Server image 22.04.5 • openSUSE Leap 15.6

Date issued: 11/05/2024





Arm SystemReady certification: SystemReady IR v1.1 (level 0)

This document certifies that the following company and system have passed the specified version of the Architecture Compliance Suite (ACS), in the below-stated SystemReady band.

Company:	Advantech
System:	Advantech RSB-3720WQ
SoC family:	NXP i.MX8M Plus
Errata:	Certification errata
Firmware version:	 Advantech firmware 3720A1AIM40LIVB0009+g84d81e0e56 (based on U-Boot v2021.04)
ACS version:	ACS-IR v1.0
BSA details:	BSA v1.0
EBBR details:	EBBR v2.0.1
BBR details:	BBR v1.0
Tested operating systems:	Fedora IoT 34openSUSE Tumbleweed
Date issued:	10-13-2021



