

Certificate



Functional
Safety

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ID 060000000

No.: 968/FSP 1503.00/18

Product tested	General purpose microprocessor design including safety features	Certificate holder	ARM Ltd. 110 Fulbourn Road Cherry Hinton CB1 9NJ Cambridge United Kingdom
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Type designation	ARM Cortex-R5 Processor Revision r1p3		
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Codes and standards	IEC 61508 Parts 1-7:2010 (in extracts)	ISO 26262 Parts 1-10:2011 (in extracts)
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Intended application	The ARM Cortex-R5 Processor complies with the requirements of IEC 61508 for SIL 3 regarding the avoidance of systematic faults for a Compliant Item and complies with the requirements of ISO 26262 for ASIL D regarding the avoidance of systematic faults for a Safety Element out of Context (SEooC). As a result of this the Cortex-R5 Processor can be used in safety-related applications up to SIL 3 according IEC 61508 and up to ASIL D according to ISO 26262.		
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Specific requirements	The requirements and constraints mentioned in the Cortex-R5 Safety Manual have to be taken into account by the user.		
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Valid until 2023-01-19

The issue of this certificate is based upon an examination, whose results are documented in Report No. 968/FSP 1503.00/18 dated 2018-01-19.

This certificate is valid only for products which are identical with the product tested.

TÜV Rheinland Industrie Service GmbH
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Köln, 2018-01-19

Certification Body Safety & Security for Automation & Grid

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